

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	0	("US-5539780-\$ DID. ORUS-5905757-\$ DID. ORUS-6195782-\$ DID. ORUS-5537445-\$ DID.").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/10 21:51
L2	8	US-5539780-\$ DID. OR US-5905757-\$ DID. OR US-6195782-\$ DID. OR US-5537445-\$ DID.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 21:52
L3	0	2 and arithmetic and table	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 21:52
L4	0	2 and arithmetic	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 21:54
L5	431	arithmetic with table with processor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 22:27
L6	13	arithmetic with table with processor and (comput\$3 adj operation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 21:59
L8	32	7 not 6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 22:24
L9	8	US-5539780-\$ DID. OR US-5905757-\$ DID. OR US-6195782-\$ DID. OR US-5537445-\$ DID.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 22:24

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L10	1136	arithmetic with (obtain\$3 receiv\$3 read\$3) adj data with (table memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 22:28
L11	2419	arithmetic with (stor\$3) adj data with (table memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 23:03
L12	243	11 and 10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 22:28
L13	68	((BURKHARD) near2 (BECKER)). INV.	US-PGPUB; USPAT	OR	ON	2006/12/10 22:36
L14	11	(arithmetic adj2 unit alu) near5 (stor\$3 near4 (obtain\$3 receiv\$3 read\$3)) adj data with (table memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 23:15
L15	5	preselect\$3 adj base adj address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/11 00:41
L16	140	base adj memory adj device	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 23:25
L17	2	16 and (arithmetic alu) and (cpu processor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 23:28
L18	363	circuit adj configuration with (arithmetic alu)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 23:29

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L19	809	(711/220).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/10 23:29
L20	0	("18and19").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/12/10 23:29
L21	74	circuit adj configuration with (arithmetic adj2 unit alu)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 23:37
L22	7	consecutive adj read adj access with consecutive adj write adj access	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 23:42
L23	120	(operation instruction) with preselect\$3 adj3 address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 23:43
L24	15	(operation instruction) with preselect\$3 adj3 address with comput\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/10 23:43